

**Mixed mode analog/digital programmable interconnect architecture.****Publication number:** EP0499383**Publication date:** 1992-08-19**Inventor:** EL AYAT KHALED A (US)**Applicant:** ACTEL CORP (US)**Classification:****- International:** H01L21/82; G06J1/00; H01L21/822; H01L27/04; H03K17/00; H03K19/173; H01L21/70; G06J1/00; H01L27/04; H03K17/00; H03K19/173; (IPC1-7): H03K19/177**- European:** G06J1/00; H03K17/00M; H03K19/173C1A**Application number:** EP19920300774 19920129**Priority number(s):** US19910654966 19910213**Also published as:**

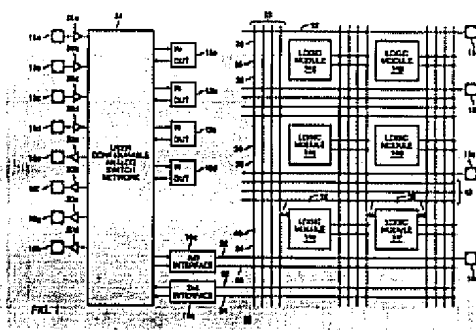
US5107146 (A1)  
JP5267458 (A)  
EP0499383 (A3)  
EP0499383 (B1)

**Cited documents:**

EP0394575  
US4758745  
US4758985  
US4860012  
US4873459  
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A user-programmable integrated circuit includes an analog portion containing user-configurable analog circuit modules, a digital portion containing user-configurable digital circuit modules, an interface portion containing user-configurable interface circuits for conversion of signals from analog to digital form and from digital to analog form, and a user-configurable interconnection and input/output architecture.



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